

Abstract of the Disclosure:

A circuit configuration for signal balancing in antiphase bus drivers, particularly for a CAN bus, which have, in each driver path of the bus, a driver amplifier unit and an output
5 stage, driven by the latter, having a power transistor circuit for transmitting an antiphase signal using a two-wire line. A control circuit is connected to one of the driver paths and controls the turn-on resistance of the power transistor circuit in this driver path such that the power transistor
10 circuits in the two driver paths have the same turn-on resistance.

WHS:tk-MUH-12862F//10/27/2003